



FPGAs: World Class Designs

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All the design and development inspiration and direction a hardware engineer needs in one blockbuster book! Clive "Max" Maxfield renowned author, columnist, and editor of PL DesignLine has selected the very best FPGA design material from the Newnes portfolio and has compiled it into this volume. The result is a book covering the gamut of FPGA design from design fundamentals to optimized layout techniques with a strong pragmatic emphasis. In addition to specific design techniques and practices, this book also discusses various approaches to solving FPGA design problems and how to successfully apply theory to actual design tasks. The material has been selected for its timelessness as well as for its relevance to contemporary FPGA design issues.

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*Hand-picked content selected by Clive "Max" Maxfield, character, luminary, columnist, and author

*Proven best design practices for FPGA development, verification, and low-power

*Case histories and design examples get you off and running on your current project

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